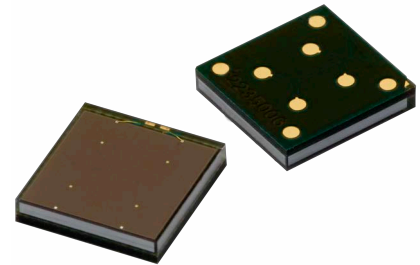


# AFBR-S4N66P014M

## NUV-MT Silicon Photomultiplier



### Description

The Broadcom<sup>®</sup> AFBR-S4N66P014M is single-channel silicon photomultiplier (SiPM) that is used for ultra-sensitive precision measurements of single photons. This SiPM is based on the NUV-MT technology, which combines improved photo-detection efficiency (PDE) with a decreased dark count rate and reduced crosstalk compared to the NUV-HD technology. The SPAD pitch is 40  $\mu\text{m}$ .

Larger areas can be covered by tiling multiple AFBR-S4N66P014M SiPMs. The encapsulation for good mechanical stability and robustness is realized by an epoxy clear mold compound, which is highly transparent down to UV wavelengths, resulting in a broad response in the visible light spectrum with high sensitivity toward the blue and near-UV region of the light spectrum. The SiPM is best suited for the detection of low-level pulsed light sources, especially for detection of Cherenkov or scintillation light from the most common organic (plastic) and inorganic scintillator materials (for example, LSO, LYSO, BGO, NaI, CsI, BaF, LaBr<sub>3</sub>). This product is lead-free and compliant with RoHS.

### Features

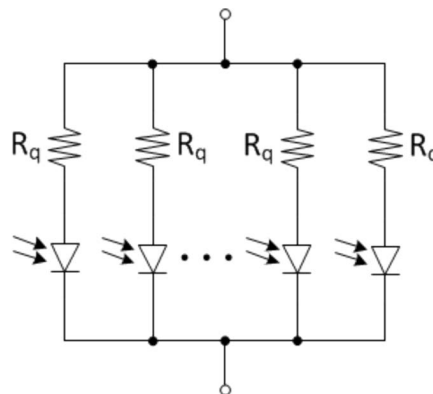
- High PDE (63% at 420 nm)
- 4-side tileable, with high fill factors
- Cell pitch: 40  $\mu\text{m}$
- Highly transparent epoxy protection layer
- Operating temperature range from  $-20^{\circ}\text{C}$  to  $+60^{\circ}\text{C}$
- Excellent SPTR and CRT
- Excellent uniformity of breakdown voltage and gain between devices
- RoHS, CFM, and REACH compliant

### Applications

- X-ray and gamma-ray detection
- Nuclear medicine
- Positron emission tomography
- Safety and security
- Physics experiments
- Cherenkov detection

### Block Diagram

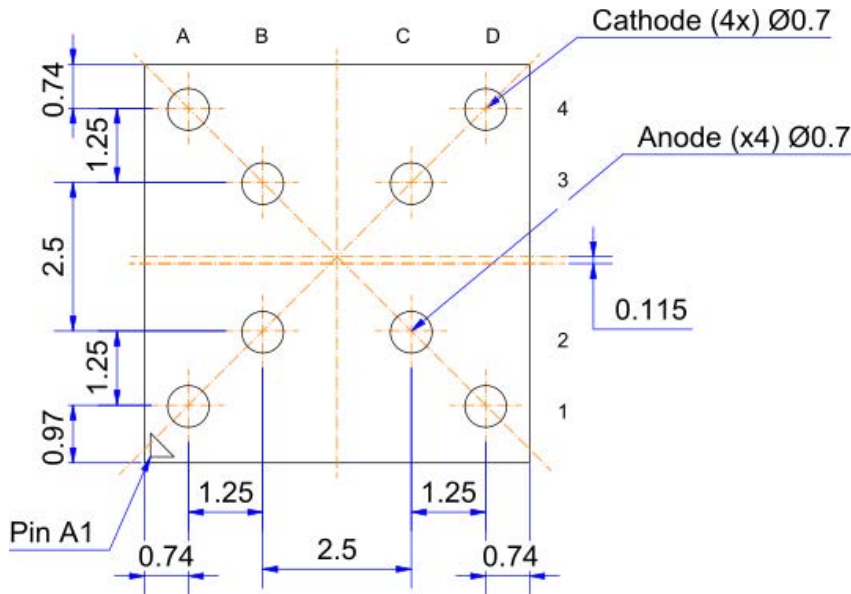
Figure 1: AFBR-S4N66P014M Block Diagram



# Pad Layout

The AFBR-S4N66P014M has four anode pads and four cathode pads. The pad layout is displayed in the following figure.

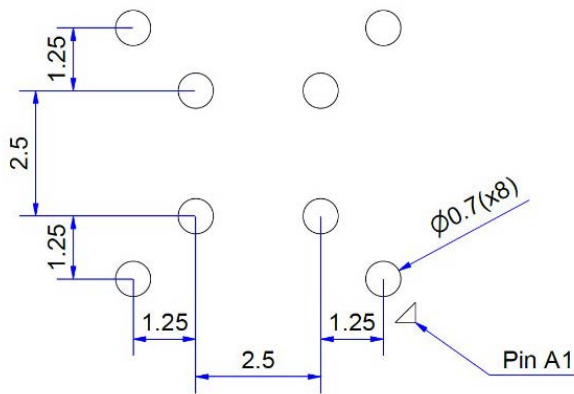
Figure 2: Pad Layout



Pad	Function
A1,A4,D1,D4	Cathode
B2,B3,C2,C3	Anode

**NOTE:** Dimensions are in mm.

Figure 3: Recommended Landing Pattern



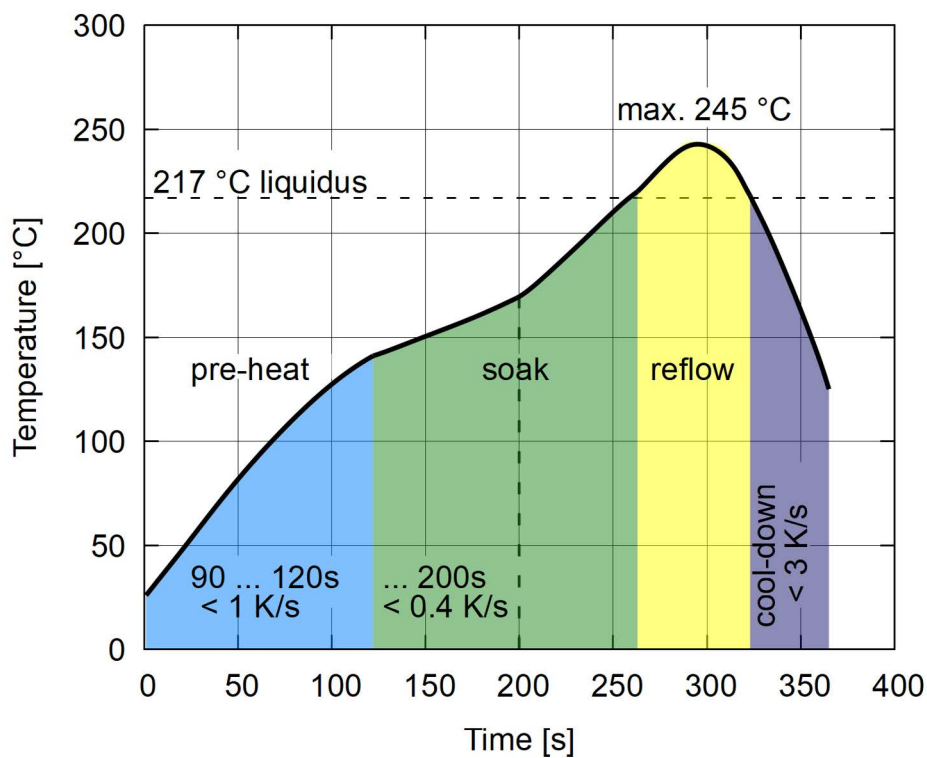
**NOTE:** The center of the footprint is not the center of the package. See [Figure 12](#) for details.

## Regulatory Compliance Table

Feature	Test Method	Performance
Electrostatic Discharge (ESD) to the Electrical Pins, Human-Body Model (contact ESD)	JESD22-A114	See <a href="#">Absolute Maximum Ratings</a> .
Electrostatic Discharge (ESD) to the Electrical Pins, Charged-Device Model	JESD22-C101F	See <a href="#">Absolute Maximum Ratings</a> .
Restriction of Hazardous Substances Directive	RoHS Directive 2011/65/EU Annex II	Certified compliant.

## Reflow Soldering Diagram

Figure 4: Recommended Reflow Soldering Profile



## Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause damage to the devices. Limits apply to each parameter in isolation. Absolute maximum ratings are those values beyond which damage to the device may occur if these limits are exceeded for other than a short period of time.

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	$T_{SG}$	-20	+60	°C
Operating Temperature <sup>a</sup>	$T_A$	-20	+60	°C
Soldering Temperature <sup>b, c</sup>	$T_{SOLD}$	—	245	°C
Lead Soldering Time <sup>b, c</sup>	$t_{SOLD}$	—	60	s
Electrostatic Discharge Voltage Capability (HBM)	$ESD_{HBM}$	—	2	kV
Electrostatic Discharge Voltage Capability (CDM)	$ESD_{CDM}$	—	500	V
Operating Overvoltage	$V_{OV}$	—	16	V

a. Biased at constant voltage = 12V above breakdown.

b. The tile is reflow solderable according to the solder diagram shown in [Figure 4](#).

c. The moisture level definition (MLD) is according to MSL 5. The floor life is 48 hours at 30°C and 60% relative humidity. No baking is required before soldering unless the floor life is exceeded. For more details on soldering and handling, refer to the Broadcom *AFBR-S4NxxPyy4M: NUV-MT Handling and Soldering* application note (AFBR-S4NxxPyy4M-AN).

## Single Device Specification

Features are measured at 25°C unless otherwise specified.

## Geometric Features

Parameter	Symbol	Value	Unit
Package Outer Dimensions	PD	6.48 x 6.71	mm <sup>2</sup>
Single Device Area	DA	6.14 x 6.14	mm <sup>2</sup>
Active Area	AA	6.00 x 6.00	mm <sup>2</sup>
Micro Cell Pitch	$L_{CELL}$	40	µm
Number of Micro Cells per Element	$N_{CELLS}$	22,428	—

## Optical and Electrical Features

Features are measured at 12V OV and 25°C unless otherwise specified.

Parameter	Symbol	Min.	Typical <sup>a</sup>	Max.	Unit	Reference Plots
Spectral Range	$\lambda$	250	—	900	nm	<a href="#">Figure 5</a>
Peak Sensitivity Wavelength	$\lambda_{PK}$	—	420	—	nm	<a href="#">Figure 5</a>
Breakdown Voltage	$V_{BD}$	32	32.5	33	V	<a href="#">Figure 7</a>
Temperature Coefficient of Breakdown Voltage	$\Delta V_{BD}/\Delta T$	—	30	—	mV/°C	—
Photo-Detection Efficiency <sup>b</sup>	PDE	—	63	—	%	<a href="#">Figure 5</a> , <a href="#">Figure 6</a>
Dark Current per Element	$I_D$	—	8.6	—	$\mu A$	<a href="#">Figure 7</a>
Dark Count Rate per Element <sup>c</sup>	DCR	—	4.4	—	Mcps	<a href="#">Figure 8</a>
Dark Count Rate per Unit Area	$DCR_{mm^2}$	—	125	—	kcps/mm <sup>2</sup>	—
Gain	G	—	7.3	—	$\times 10^6$	<a href="#">Figure 9</a>
Optical Crosstalk	$P_{XTALK}$	—	23	—	%	<a href="#">Figure 10</a>
Afterpulsing Probability	$P_{AD}$	—	< 1	—	%	<a href="#">Figure 10</a>
Recharge Time Constant	$T_{FALL}$	—	55	—	ns	<a href="#">Figure 11</a>
Nominal Terminal Capacitance <sup>d</sup>	$C_T$	—	1550	—	pF	—
Temperature Coefficient of Gain <sup>e</sup>	$\Delta G/\Delta T$	—	1.46	—	$\times 10^4/^\circ C$	—

a. Measured at 12V OV.

b. Measured at the peak sensitivity wavelength. Measurement does not include correlated noise, such as afterpulsing or optical crosstalk.

c. Measured at 0.5 p.e. amplitude. Measurement does not include delayed correlated events.

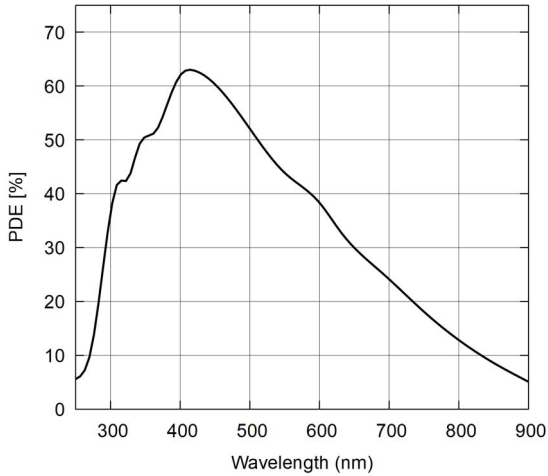
d. Measured using the input sine wave with  $f = 200$  kHz and  $V_{in} = 500$  mV.

e. Calculated from the gain dependence on V and the breakdown voltage temperature coefficient:  $dG/dT = dG/dV \times dV_{BD}/dT$ .

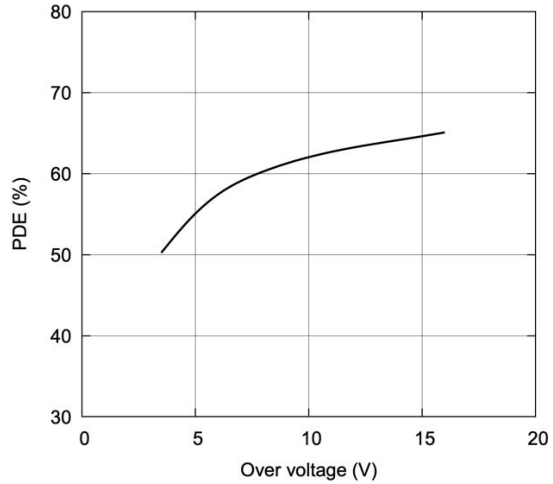
# Reference Plots

Features are measured at 25°C unless otherwise specified. Plotted data represents typical values.

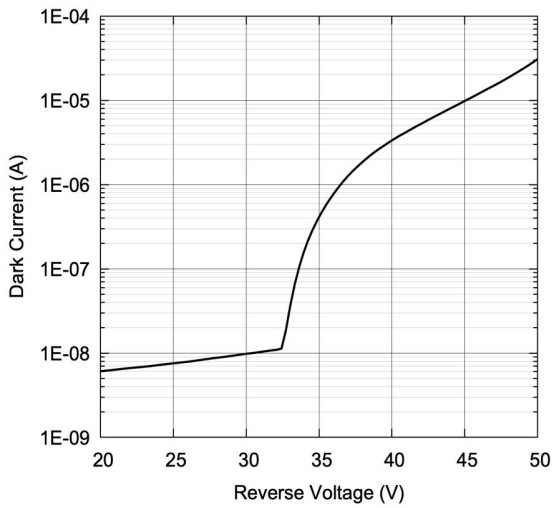
**Figure 5: PDE vs. Wavelength**



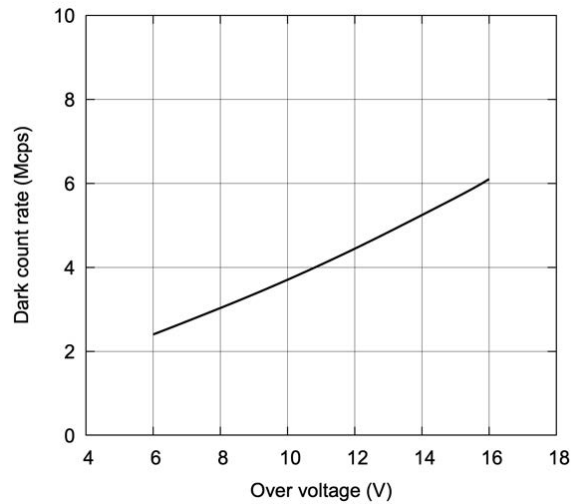
**Figure 6: PDE at Peak  $\lambda$  vs. OV**



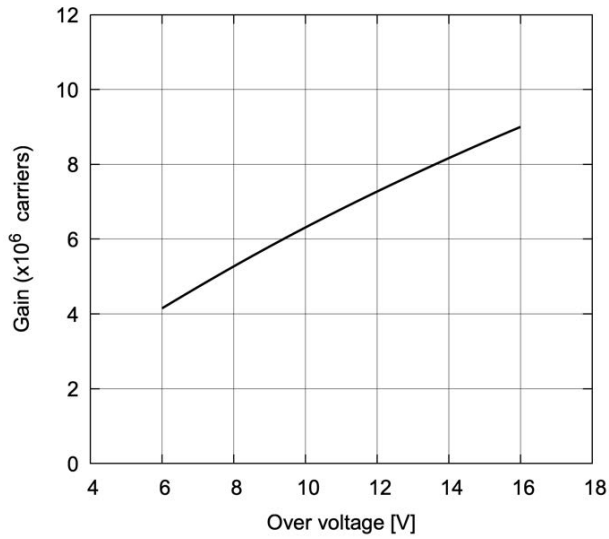
**Figure 7: Reverse IV Curve**



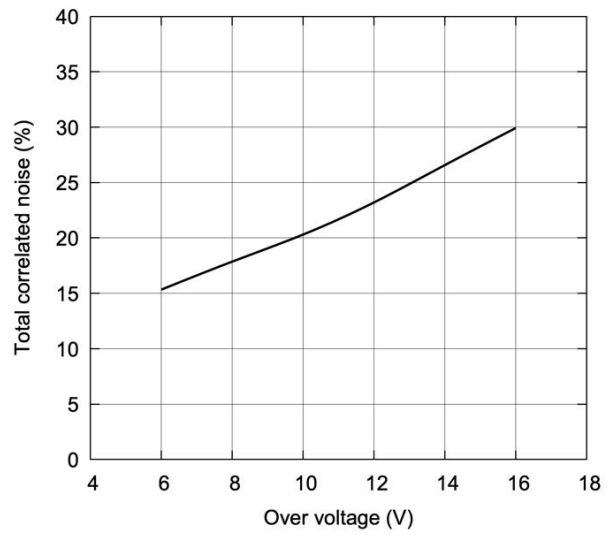
**Figure 8: Dark Count Rate vs. OV**



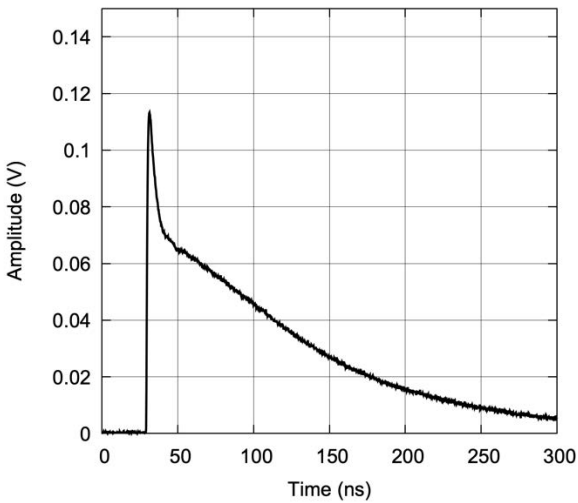
**Figure 9: Gain vs. OV**



**Figure 10: Correlated Noise vs. OV**

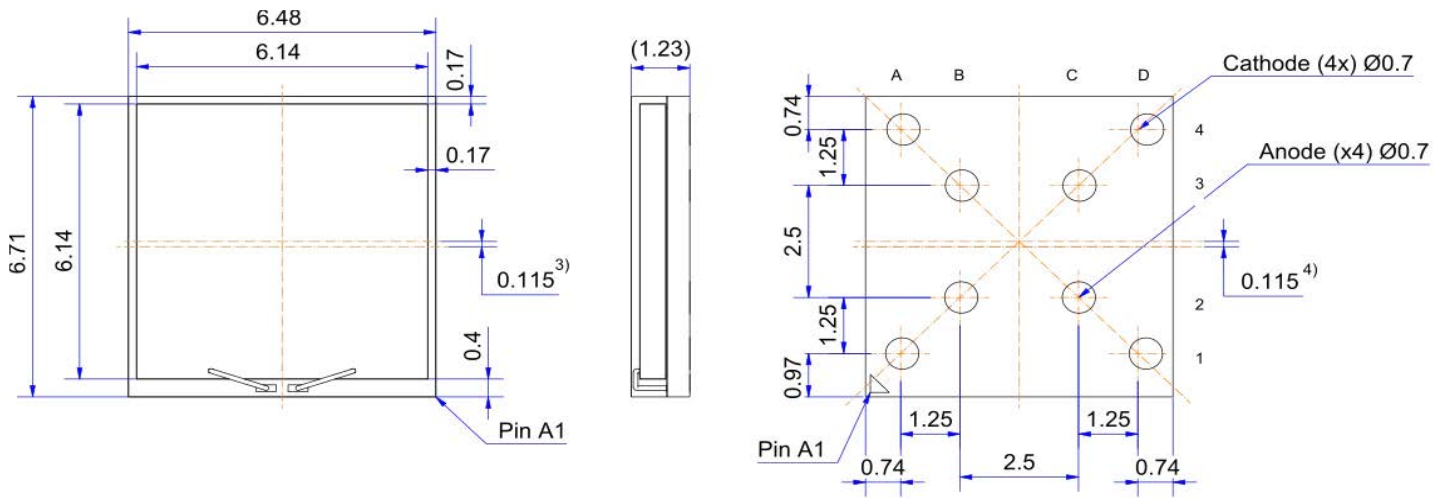


**Figure 11: Example Signal Measured at 12V OV**



# Mechanical Data – Package Outline

Figure 12: Package Outline Drawing (Dimensions in mm, Numbers Rounded to Two Decimal Places)



NOTES

- 1) Dimensions are in millimeters.
- 2) Nominal values rounded to two decimal places - Suppression of following zeros
- 3) Distance from chip center to package center
- 4) Distance pad geometry to package center

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